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Skills:

- Advanced operation and administration of DOS 6.x, Windows 3.x and 9x/NT/2000/XP, and UNIX/Linux/Cygwin.
- Intermediate programming in Assembly (Sun SPARC), FORTRAN, Pascal, C/C++ , Java, and Visual Basic.
- Intermediate scripting in DOS batch files, Perl and UNIX Bash shell.
- Advanced desktop publishing in Microsoft Office, OpenOffice, Adobe PageMaker and Photoshop, and The GIMP.
- Intermediate authoring of World Wide Web home pages in HTML, CSS, and PHP and administration in Apache Web Server.
- Intermediate data analysis and scripting in Microsoft Excel, Microcal Origin, and MATLAB.
- Intermediate analog design and simulation in Orcad PSpice and Microwave Office, basic simulation in Cadence.
- Intermediate analog chip layout in Microwave Office and Cadence and digital chip layout in Magic.
- Basic logic design in Xilinx Foundation Tools, VHDL, and Verilog.
- Basic circuit design and PCB layout in Orcad Capture and Layout.
- Intermediate RF test engineering on standalone instruments and automation on LabWindows/CVI and TestStand.
- Advanced assembly, upgrade, and repair of x86 PCs.
- Intermediate technical support for personal and business computer systems and company hardware products.
- Intermediate tutorial skills in math and science; university-level math, physics, electrical and computer engineering.

Educational Background:

- 20 Mar 2004: M.Eng. (Master of Engineering) in Electrical Engineering at the University of California at San Diego (UCSD), La Jolla, CA.
- 14 Jun 1998: B.S. in Bioengineering at UCSD.

Experience:

22 Jun 2002 to present: **RF Engineer**, Northrop Grumman Space Technology, San Diego, CA.

RF integrated circuit engineering. Worst-case analysis in PSpice. Physical chip layout design in Microwave Office and Cadence and PCB layout in Orcad Layout. Documentation in Microsoft PowerPoint, Word, Excel, and Postscript/PDF. Administration of foundry design kits in Windows, Unix, HTML. Test automation using LabWindows/CVI and TestStand.

09 Apr 2001 to 21 Jun 2002: **Graduate Intern**, Northrop Grumman Space Technology, San Diego, CA.

RF integrated circuit engineering support. Worst-case analysis, physical chip layout design, PCB layout design, and documentation.

03 Apr 2000 to 01 Sep 2000: **Customer Support Technician**, Posiflex Business Machines, Union City, CA.

Hardware technical support for Posiflex products. Repair of damaged/defective products. Development of testing software for products in Visual Basic.

14 Feb 2000 to 30 Mar 2000: **Computer Accessory Salesperson**, Fry's Electronics, Fremont, CA.

Customer assistance and sales in the computer accessory department.

31 Aug 1998 to 15 Dec 1999: **Assistant Scientist**, Apovia, San Diego, CA.

Purification of DNA and RNA oligonucleotides through gel electrophoresis (PAGE) and cartridge columns (C18). Management of PAGE department, including purchasing of reagents and supplies. In-house technical support for laboratory computer systems. Quality control of in-house and purified products at a cGMP production level.

02 Aug 1998 to 04 Sep 1998 and

29 Jul 1996 to 30 Aug 1996: **Math/Science Workshop Assistant**, Summer Bridge Program of UCSD.

Co-leadership of one of ten Math/Science workshops in Summer Bridge, a retention program for incoming freshmen, a part of the Office of Academic Support and Instructional Services (OASIS) at UCSD. Teaching and tutoring in basic math and science concepts, including statistics, nutrition, and economics.

07 Oct 1996 to 25 Mar 1998: **Math Tutor**, UCSD OASIS for the regular academic year.

Tutoring in pre-calculus, calculus, differential equations, statistics, and concepts in physics and circuits.

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